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S1	2	"6378066".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 11:10
S2	1	US20020162089A1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/15 12:51
S3	2	"6286135".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/15 12:56
S4	2	"6493863".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/15 12:56
S5	2	"6662278".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/15 13:09
S6	2	"6449711".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/15 13:09
S7	95	("4875451" "4922418" "5007732" "5277192" "5390286" "5515327" "5712996" "6119721" "6141313" "4315315" "4400846" "4609872" "4764925" "4800889" "4847755" "4855903" "5021947" "5241635" "5277196" "5301141" "5369775" "5388189" "5448598" "5454104" "5457640" "5507412" "5524629" "5562101" "5603012" "5616840" "5703793" "5727233" "5734586" "5740460" "5742814" "5765037" "5768561" "5768629" "5784631" "5798719" "5809270" "5829007" "5842033" "5857183" "5870588" "5872949" "5881301" "5929858" "5938748" "5952569").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 11:10

S8	2	"5963972".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 12:14
S9	2	"5535318".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 12:31
S10	2	"5457806".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 12:15
S11	2	"5539907".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 12:31
S12	4	("5963972").URPN.	USPAT	OR	OFF	2005/03/16 12:45
S13	4	("5450564" "5539907" "5737750" "5737752").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/16 12:59
S14	122	graph with node\$1 with dependenc\$3 with edg\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:11
S15	70	S14 and (node\$1 with (block\$1 or segment\$1 or code\$1 or unit\$1 or instruction\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:12
S16	57	S15 and (dependenc\$3 with (block\$1 or segment\$1 or code\$1 or unit\$1 or instruction\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:13
S17	0	S16 and (execut\$3 with node\$1 with (color\$1 or visual\$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:21
S18	1	S16 and (execut\$3 with (color\$1 or visual\$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:15

S19	1	S16 and (execut\$3 with node\$1 same (color\$1 or visual\$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:19
S20	1	S16 and (color\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:19
S21	7	S16 and (execut\$3 with node\$1) and (color\$1 or visual\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:29
S22	0	S16 and (display\$3 with execut\$3 with node\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:30
S23	0	S16 and (display\$3 with unexecut\$3 with node\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:30
S24	618	(display\$3 with execut\$3 with node\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:30
S25	0	S24 and (graph with node\$1 with edge\$1 with dependenc\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:31
S26	28	S24 and (graph with node\$1 with edge\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:31
S27	307	717/151.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 16:25
S28	102	717/155.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 16:25

S29	146	717/156.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 16:25
S30	62	717/157.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 16:26
S31	341	711/129.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 16:26


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- 1 [David vs. Goliath or mice vs. men? \(panel session\): production studio size in the entertainment industry](#)

Pauline Ts'o, Theresa Ellis, Ralph Guggenheim, Brad Lewis, Ron Thornton

 September 1995 **Proceedings of the 22nd annual conference on Computer graphics and interactive techniques**

 Full text available: [pdf\(221.71 KB\)](#) Additional Information: [full citation](#), [index terms](#)

- 2 [Production for the long haul](#)

John C. Donkin, Charles Gibson, Ralph Guggenheim, Edward Kummer, Brad Lewis, Jeff Thingvold

 July 1994 **Proceedings of the 21st annual conference on Computer graphics and interactive techniques**

 Full text available: [pdf\(18.27 KB\)](#) Additional Information: [full citation](#), [index terms](#)

- 3 [Panel: object oriented requirements analysis vs. structured analysis as a front end for object oriented design](#)

Mitchell J. Bassman, John Anderson, Brad Balfour, Lewis Gray, Kent Johnson, Ken Shumate

 July 1992 **Proceedings of the ninth Washington Ada symposium on Ada: Empowering software users and developers**

 Full text available: [pdf\(650.11 KB\)](#) Additional Information: [full citation](#), [references](#)

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1 [Managing graph\(ical\) complexity with raisin and its category explorer](#)

Douglas N. Gordin

June 2002 **Proceedings of the 2nd international symposium on Smart graphics**Full text available:  pdf(12.70 MB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Graphs offer a powerful way to view the relationships between objects. Yet, as useful as small graphs are for seeing relationships, large graphs are frustrating because their complexity overwhelms our ability to trace through patterns of relationships. The *Raisin* system helps manage this complexity by giving the means to layout a graph well; index a graph using categories based on structure and other criteria; highlight and abstract via selection, hiding, and aggregation; and create new c ...

Keywords: aggregation, categorization, directed acyclic graph, focus+context, graph visualization, simplification, tree control

2 [New frontiers in logic synthesis: Multiple constant multiplication by time-multiplexed mapping of addition chains](#)

Peter Tummeltshammer, James C. Hoe, Markus Püschel

June 2004 **Proceedings of the 41st annual conference on Design automation**Full text available:  pdf(119.84 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

An important primitive in the hardware implementations of linear DSP transforms is a circuit that can multiply an input value by one of several different preset constants. We propose a novel implementation of this circuit based on combining the *addition chains* of the constituent constants. We present an algorithm to automatically generate such a circuit for a given set of constants. The quality of the resulting circuits is evaluated after synthesis for a commercial 0.18um standard cell li ...

Keywords: addition chains, directed acyclic graph, fusion, multiplierless

3 [Task scheduling using a block dependency DAG for block-oriented sparse Cholesky factorization](#)

Heejo Lee, Jong Kim, Sung Je Hong, Sunggu Lee

March 2000 **Proceedings of the 2000 ACM symposium on Applied computing**Full text available:  pdf(790.75 KB)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: block-oriented Cholesky factorization, directed acyclic graph, parallel sparse



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Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [next](#)Relevance scale ☐ ☐ ☐ ☐ ☐**1** [A Survey of Some Theoretical Aspects of Multiprocessing](#)

J. L. Baer

January 1973 **ACM Computing Surveys (CSUR)**, Volume 5 Issue 1Full text available: [pdf\(4.05 MB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**2** [Compiling nested data-parallel programs for shared-memory multiprocessors](#)

Siddhartha Chatterjee

July 1993 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 15 Issue 3Full text available: [pdf\(4.17 MB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)**Keywords:** compilers, data parallelism, shared-memory multiprocessors**3** [Technical reports](#)

SIGACT News Staff

January 1980 **ACM SIGACT News**, Volume 12 Issue 1Full text available: [pdf\(5.28 MB\)](#)Additional Information: [full citation](#)**4** [Tutorial: Compiling concurrent languages for sequential processors](#)

Stephen A. Edwards

April 2003 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 8 Issue 2Full text available: [pdf\(771.65 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Embedded systems often include a traditional processor capable of executing sequential code, but both control and data-dominated tasks are often more naturally expressed using one of the many domain-specific concurrent specification languages. This article surveys a variety of techniques for translating these concurrent specifications into sequential code. The techniques address compiling a wide variety of languages, ranging from dataflow to Petri nets. Each uses a different method, to some degree ...

Keywords: Compilation, Esterel, Lustre, Petri nets, Verilog, code generation, communication, concurrency, dataflow, discrete-event, partial evaluation, sequential